

ABSTRACT OF THE DISCLOSURE

A receiver for the CDMA system, in order to reduce a power consumption during a suspension period of intermittent receiving operation, monitors a suspension period t_1 by means of a low-power timer 51 so that a VC-TCXO 1, a reference signal group generation unit 2 and a receiving unit 3 are turned off and a modem unit 4 is set to a sleep state. Upon resumption of receiving operation, a high-accuracy timer 44 is supplied with a start (d) to require counting of time t_3 and a part of a received signal is stored. A PN code phase of stored data is calculated in a PN code phase calculator 46 during a period t_3 to obtain an indication value i for a phase deviation. State vectors for short code and long code and further a reception time t_4 are calculated on the basis of the indication value i to be set. When the high-accuracy timer 44 counts t_3 and produces $OV(d)$, demodulation operation by a rake demodulation unit 40, a demultiplexing unit 41 and a descrambler unit 42 is started in accordance with the setting and is continued during the reception time t_4 .

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